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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/614,604	07/07/2003	Naomi M. Jenkins	2000.107500/TT5487	7792
	7590 04/04/200 IORGAN & AMERSO	EXAMINER		
10333 RICHMOND, SUITE 1100			VO, HIEN XUAN	
HOUSTON, TX 77042			ART UNIT	PAPER NUMBER
			2863	
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			04/04/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

	Application No.	Applicant(s)
	10/614,604	JENKINS ET AL.
Office Action Summary	Examiner	Art Unit
	HIEN X. VO	2863
The MAILING DATE of this communication a Period for Reply	ppears on the cover sheet with the	correspondence address
A SHORTENED STATUTORY PERIOD FOR REP WHICHEVER IS LONGER, FROM THE MAILING - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period. - Failure to reply within the set or extended period for reply will, by state Any reply received by the Office later than three months after the mail earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATIO 1.136(a). In no event, however, may a reply be tied will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDON	N. mely filed n the mailing date of this communication. ED (35 U.S.C. § 133).
Status		
1) Responsive to communication(s) filed on 28	nis action is non-final. vance except for formal matters, pr	
Disposition of Claims		
4) ☐ Claim(s) 1-50 is/are pending in the application 4a) Of the above claim(s) is/are withdrest of the above claim(s) is/are withdrest of the above claim(s) is/are allowed. 6) ☐ Claim(s) 1,2,4,13,16-18,20-23,25,26,38-44 and and allowed. 7) ☐ Claim(s) 3,5-12,14,15,19,24,27-37,45,49 and and allowed are subject to restriction and allowed are subject to restriction and application Papers 9) ☐ The specification is objected to by the Examination of the description of the applicant may not request that any objection to the application of the application for the application of the	rawn from consideration. and 46-48 is/are rejected. d 50 is/are objected to. /or election requirement. ner. a) ☑ accepted or b) ☐ objected to be drawing(s) be held in abeyance. Se	ee 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the I		•
Priority under 35 U.S.C. § 119		
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a list	nts have been received. nts have been received in Applica iority documents have been receiv au (PCT Rule 17.2(a)).	tion No red in this National Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date	4) Interview Summar Paper No(s)/Mail D 5) Notice of Informal 6) Other:	oate

DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1-2, 4, 13, 16-18, 20-23, 25-26, 38-39, 40-44, 46-48 rejected under 35 U.S.C. 103(a) as being unpatentable over Adams, III et al. (U.S. Patent No. 6,740,534) in view of Allen, Jr. et al. (U.S. Patent No. 7.051,250).

With respect to claims 1, 13, 16, 17, 22, 26, 38, 42, 44, 46 Adams, III et al. disclose a determination of a process flow based upon fault detection analysis including a processing tool to process a batch of workpieces (see e.g. abstract), a process controller operatively coupled to perform a tool state analysis upon said processing tool (see e.g. Fig. 3, item 310) and to perform a metrology routing adjustment process based upon said tool state analysis, said metrology routing adjustment process comprising correlating said tool state analysis to said batch of workpieces and adjusting a metrology routing based upon said correlation (see e.g. Fig. 5, item 510 and col. 6, lines 31-67, Fig.2, item 270). Adams, III et al. do not teach a dynamic metrology routing adjustment process based upon the tool state analysis, the dynamic metrology routing adjustment process further comprises correlating the tool state analysis to the batch of workpieces and adjusting a metrology routing based upon the correlation.

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Allen, Jr. et al. disclose a method and apparatus for routing workpieces based upon detecting a fault including a dynamic metrology routing adjustment process based upon the tool state analysis (see e.g. abstract), the dynamic metrology routing adjustment process further comprises correlating the tool state analysis to the batch of workpieces and adjusting a metrology routing based upon the correlation (see e.g. Figs. 1-3,). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify system of Adams, III et al. by the routing workpieces based upon detecting a fault as taught by Allen, Jr. et al. to overcome and perform separate critical dimension measurement for each pattern formed within a semiconductor device in addition to separate overlay measurement to increased throughput, reduced cost, higher device performance.

With respect to claims 2, 4, 18, 20-21, 23, 25, 39, 40-41, 43, 47-48, Adams, III et al. disclose the invention as claimed including the process step upon the batch of workpieces further comprises performing the process step upon a batch of semiconductor wafers (see e.g. Fig.1), a database unit to store at least one of metrology data, tool state data and the electrical test data (see e.g. Fig. 3. item 340), the tool state analysis upon the processing tool further comprises acquiring tool state data (see e.g. Fig.3, item 320), the tool state data further comprises acquiring at least one of a pressure data, a temperature data, a humidity data, and a gas flow rate data relating to the process step performed upon the workpieces (see e.g. col. 4, lines 64-67).

Claims 3, 5, 6-12, 14-15, 19, 24, 27-37, 45, 49-50 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in

independent form including all of the limitations of the base claim and any intervening claims.

Applicant's arguments with respect to claims 1-50 have been considered but are moot in view of the new ground(s) of rejection.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to HIEN X. VO whose telephone number is (571)272-2282. The examiner can normally be reached on M-F (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Barlow can be reached on (571) 272-2269. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Hien Vo 03/29/08 /John E Barlow Jr./ Supervisory Patent Examiner, Art Unit 2863

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